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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

<p>Sheet 1 of 1</p>				Complete if Known	
				Application Number 10/770,407	
				Filing Date February 4, 2004	
				First Named Inventor Anthony J. Magrath	
				Art Unit N/A	
				Examiner Name Not Yet Assigned	
				Attorney Docket Number M0025.0302/P302	

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
TB	/	GB 2 343 573-A	5-2000	Application Solutions Ltd.		

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TB		A.J. Magrath et al. "Design and Implementation of a FPGA Sigma-Delta Power DAC", <u>IEEE</u> , November 1997, pp. 511-521	
TB		A.J. Magrath et al., "Power Digital-to-Analogue Conversion Using Sigma-Delta Pulse Inversion Techniques", 99th Convention of the Audio Eng. Society, October 1995,	

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/Tesfaldet Bocure/

01/31/2007

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TB	CA	J. Vanderkooy, "New Concepts in Pulse-Width Modulation", Presented at AES 97th Convention, San Francisco, Nov. 10-13, 1994.			
TB	CB	A.J. Magrath et al., "Performance Enhancement of Sigma-Delta Modulator D-A Converters using Non-linear Techniques" IEEE, 1996.			
TB	CC	A.J. Magrath et al., "Digital Power Amplification using Sigma-Delta Modulation and Bit Flipping" J. Audio Eng. Soc., Vol. 45, No. 6, June 1997, pp. 476 - 487.			

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